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Proceedings of the twentieth annual ACM symposium on Principles of distributed computing August 2001

Redundant arrays of inexpensive disks (RAID) offer fault tolerance against disk failures. However a storage system having more disks suffers from less reliability and performance. A RAID architecture tolerating multiple disk failures shows severe performance degradation in comparison to the RAID Level 5 due to the complexity of implementation. We present a new RAID architecture that tolerates at least three disk failures and offers similar throughput to the RAID Level 5. We call it the hierar ...

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Proceedings of the third ACM international conference on Multimedia January 1995

- 8** The TickerTAIP parallel RAID architecture 98%
 Pei Cao , Swee Boon Lin , Shivakumar Venkataraman , John Wilkes
ACM Transactions on Computer Systems (TOCS) August 1994
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Traditional disk arrays have a centralized architecture, with a single controller through which all requests flow. Such a controller is a single point of failure, and its performance limits the maximum number of disks to which the array can scale. We describe TickerTAIP, a parallel architecture for disk arrays that distributes the controller functions across several loosely coupled processors. The result is better scalability, fault tolerance, and flexibility. This article present ...

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 Chris Nyberg , Tom Barclay , Zarka Cvetanovic , Jim Gray , Dave Lomet

ACM SIGMOD Record , Proceedings of the 1994 ACM SIGMOD international conference on Management of data May 1994
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A new sort algorithm, called AlphaSort, demonstrates that commodity processors and disks can handle commercial batch workloads. Using Alpha AXP processors, commodity memory, and arrays of SCSI disks, AlphaSort runs the industry-standard sort benchmark in seven seconds. This beats the best published record on a 32-cpu 32-disk Hypercube by 8:1. On another benchmark, AlphaSort sorted more than a gigabyte in a minute. AlphaSort is a cache-sensitive memory-intensive sort algorithm. It ...


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 Yuanyuan Zhou , Angelos Bilas , Suresh Jagannathan , Cezary Dubnicki , James F. Philbin , Kai Li
ACM SIGARCH Computer Architecture News , Proceedings of the 29th annual international symposium on Computer architecture May 2002


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This paper examines how VI-based interconnects can be used to improve I/O path performance between a database server and the storage subsystem. We design and implement a software layer, DSA, that is layered between the application and VI. DSA takes advantage of specific VI features and deals with many of its shortcomings. We provide and evaluate one kernel-level and two user-level implementations of DSA. These implementations trade transparency and generality for performance at different degrees ...

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13 Dynamic file allocation in disk arrays 95%

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